


The diagram shows a 4-to-1 multiplexer. On the left, there is a vertical rectangle representing the multiplexer body, with four input lines labeled K_1 , K_2 , K_3 , and K_4 entering from the left. On the right, there are four output lines labeled $PPJ2N1,5$, each connected to a small square box representing a switch or output terminal. The output lines are labeled K_1 , K_2 , K_3 , and K_4 from top to bottom, corresponding to the input lines.

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